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Ivan Sanchez Esqueda is an Assistant Professor in the School of Electrical, Computer, and Energy Engineering at Arizona State University (ASU). Prior to his appointment at ASU, he was a research lead at the University of Southern California (USC). His current research is focused on next-generation electronic devices, and on the integration of emerging materials to advance CMOS and beyond-CMOS technologies. His work has contributed to the development of non-volatile memory (NVM), memristors, and field-effect-transistors (FETs) based on 2D and 1D material systems. Dr. Sanchez Esqueda's work includes the characterization and modeling of advanced semiconductor technologies and their response to extreme environments including cryogenic and radiation. Dr. Sanchez Esqueda is a senior member of IEEE, has previously served as associate editor of the IEEE Transactions on Nuclear Science. He is currently an associate editor for the IEEE Transactions on Electron Devices.